MOD

DELAY

FIG. 1

DELAY CTL

ROTATE

PHASE CTL

Q

DATA OUT

36

Q

15

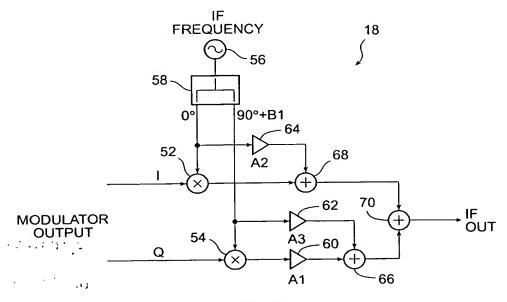


FIG. 2



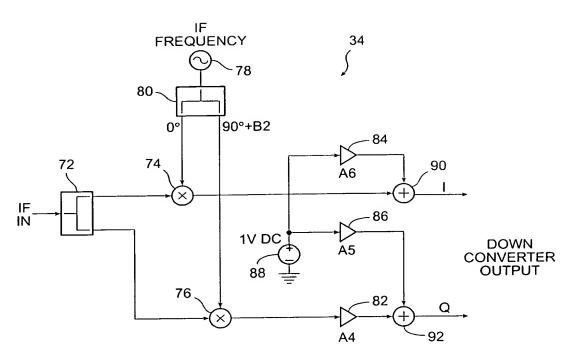


FIG. 3

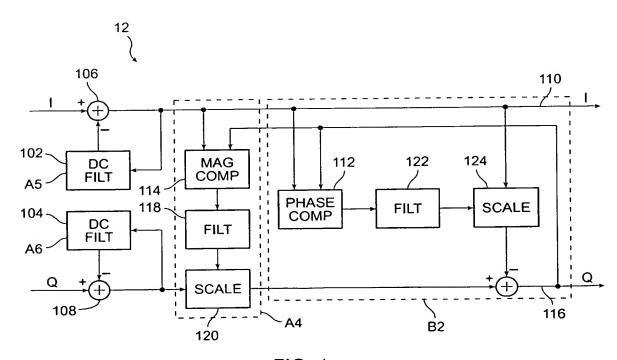


FIG. 4

Appl. No. 10/051,887 Amdt. dated July 1, 2003 Substitute Figs. 1-8D



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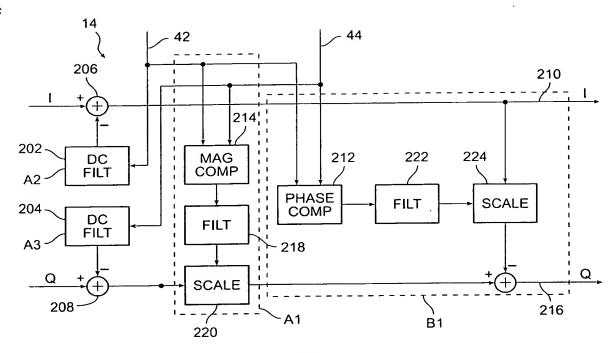


FIG. 5

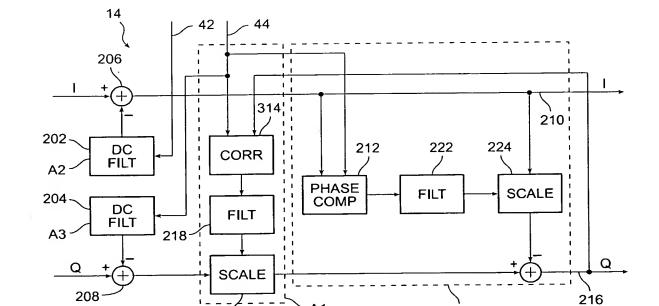


FIG. 6

В1

- A1

220

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-<u>i</u>-

Appl. No. 10/051,887
Amdt. dated July 1, 2003
Substitute Figs. 1-8D

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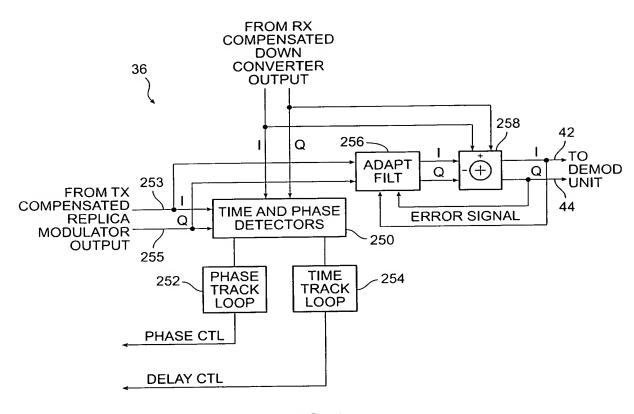
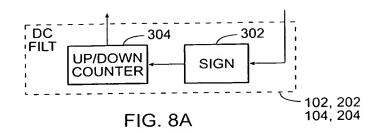


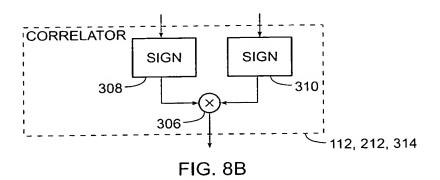
FIG. 7

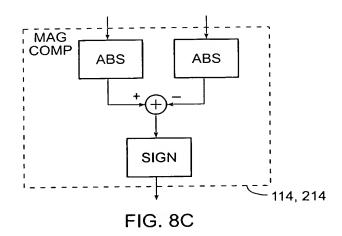
+

: 1









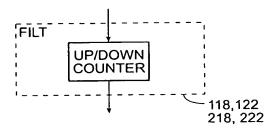


FIG. 8D